

C 7000.....Name.....

Reg. No.

SECOND SEMESTER M.Sc. DEGREE EXAMINATION, AUGUST 2005

Computer Science

CS. 203: MICROPROCESSOR AND APPLICATIONS

Time : Three Hours

Maximum : 60 Marks

Part A

*Answer any **five** questions.
Each question carries 3 marks.*

1. Give the architecture of 8086.
2. Explain DMA transfer sequence.
3. Explain Interrupt driven I/O.
4. Explain 8253 in detail.
5. Explain any *five* debugging aids.
6. Explain with example a real-time system.
7. What are the various software development tools ?

(5 x 3 = 15 marks)

Part B

*Answer any **three** questions.
Each question carries 15 marks.*

8. (a) Explain Instrumentation systems. (7 marks)
(b) Differentiate between Firmware, Software and Hardware. (8 marks)
9. (a) What are the elements of an I/O interface ? (7 marks)
(b) What are the data transfer schemes ? (8 marks)
10. (a) Describe polled data transfer. (8 marks)
(b) Differentiate between Minimum mode and Maximum mode. (7 marks)
11. (a) Explain Mnemonics and Operands. (7 marks)
(b) Explain Input Control signals. (8 marks)

[3 x 15 = 45 marks]